Associate Research Scientist
Columbia University in the City of New York

Job Title	Associate Research Scientist
Department	SEAS - Computer Science
	https://www.cs.columbia.edu/
Institution	Columbia University in the City of New York
	New York, New York
Date Posted	Mar. 11, 2024
Application Deadline	Apr. 15, 2024
Position Start Date	Jun. 1, 2024
Job Categories	Professional Staff
Academic Field(s)	Research/Technical/Laboratory
Apply Online Here	http://apply.interfolio.com/142519
Apply By Email	
Job Description	

Columbia Engineering invites applications for an Associate Research Scientist in the Department of Computer Science at Columbia University in the City of New York at all levels, to begin June 1, 2024.

The successful candidate is expected to contribute to the advancement of the research program of the System-Level Design group, which investigates the development of heterogeneous computing systems, system-on-chip platform, and open-source hardware. In particular, the successful candidate will be developing and leading externally funded research projects and contributing to the undergraduate and graduate educational mission of the department.

Qualifications

Candidates must have a Ph.D. or its professional equivalent by the starting date of the appointment. At

least two years of experience as a postdoctoral researcher in academia, or similar experience in the industry, is strongly preferred. Expertise in designing and programming of systems implemented with field-programmable gate array (FPGA) technology is a critical requirement.

1. Ability to conduct research as required with proven analytical and problem-solving skills.
2. Excellent project management skills and experience. Excellent written and oral communication skills.
3. Ability to effectively interact with and mentor undergraduate and graduate students.
4. Expertise in system-on-chip architecture design, optimization, and verification for both FPGA and ASIC implementations;
5. Expertise in FPGA-based design and implementation of high-performance accelerators for machine learning applications, high-speed communication protocols and sensor-interfacing subsystems;
6. Experience in developing open-source computer-aided design (CAD) tools for FPGA and ASIC systems, with particular emphasis on system-level design (system integration, hardware/software co-design, and performance optimization);

Hiring Salary Range: $77,000 - $135,000. The salary of the finalist selected for this role will be set based on a variety of factors, including but not limited to departmental budgets, qualifications, experience, education, licenses, specialty, and training. The above hiring range represents the University's good faith and reasonable estimate of the range of possible compensation at the time of posting.

To formally submit your application and supporting documents, please visit Columbia University's online application site at the following link: http://apply.interfolio.com/142519

EEO/AA Policy

Columbia University is an equal opportunity employer/disability/veteran.

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Direct Link: https://www.AcademicKeys.com/r?job=232716
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Contact Information

Please reference Academickeys in your cover letter when applying for or inquiring about this job announcement.

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